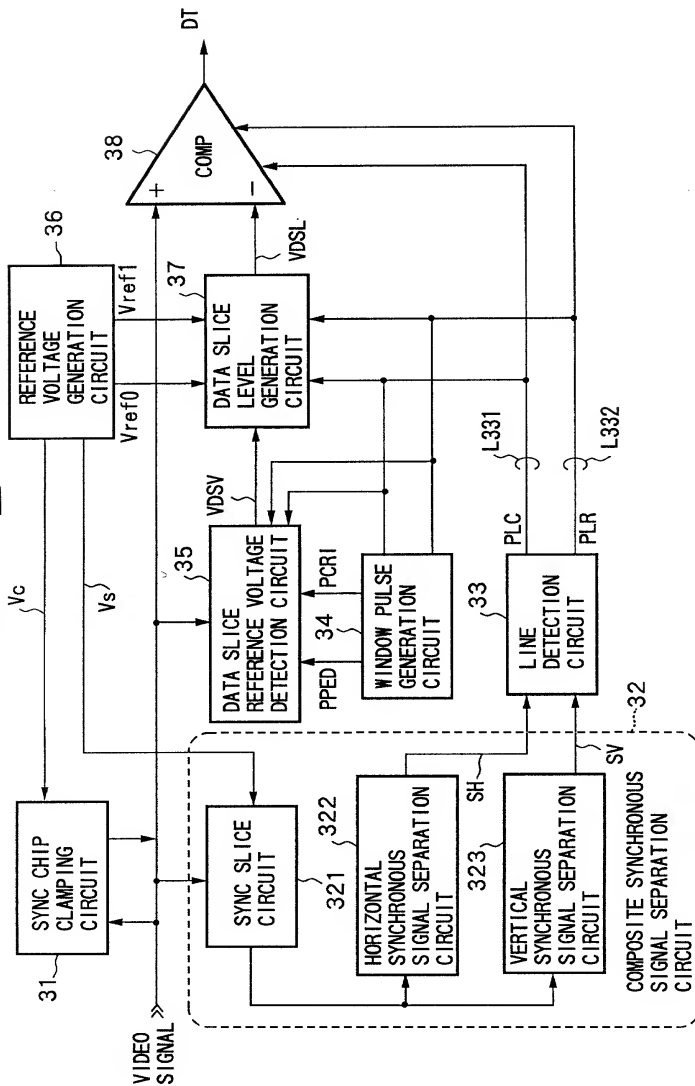


FIG.1

30



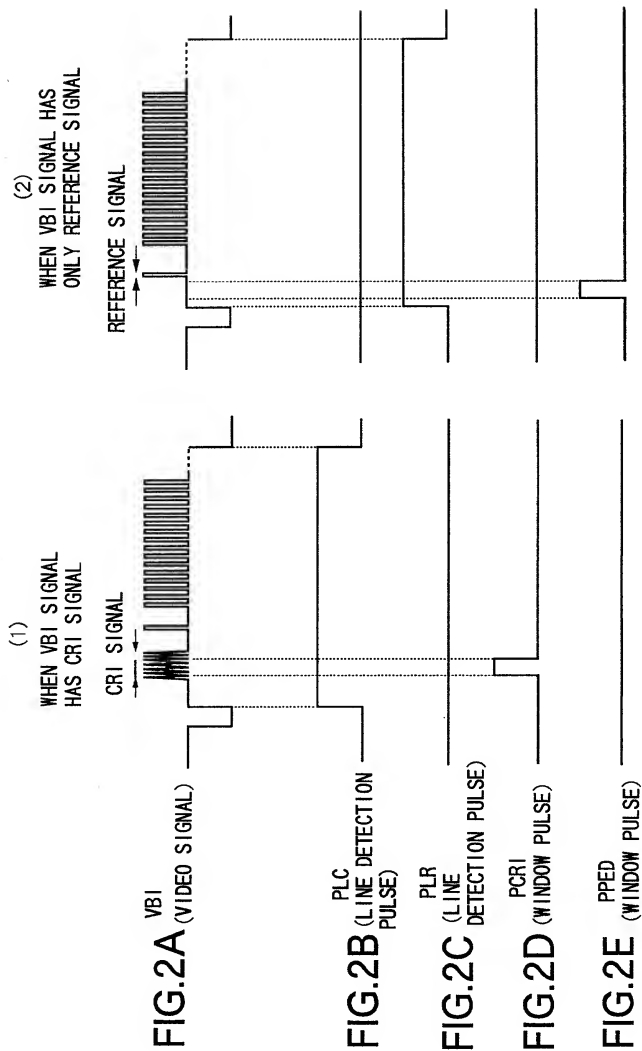
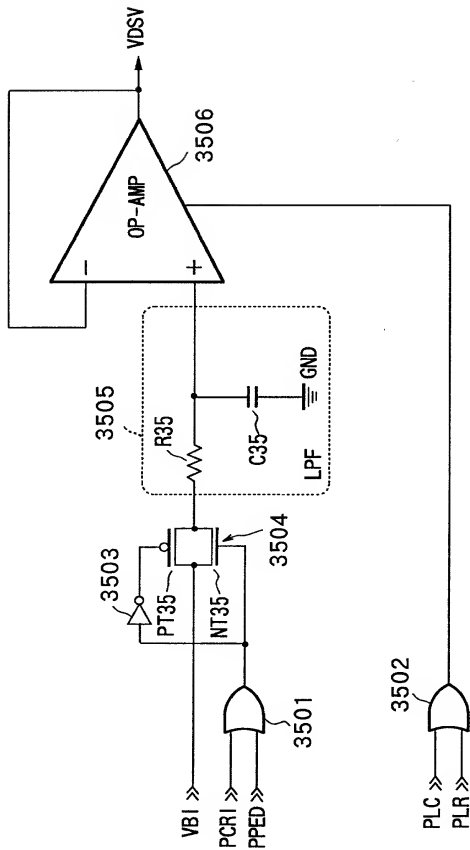


FIG.3

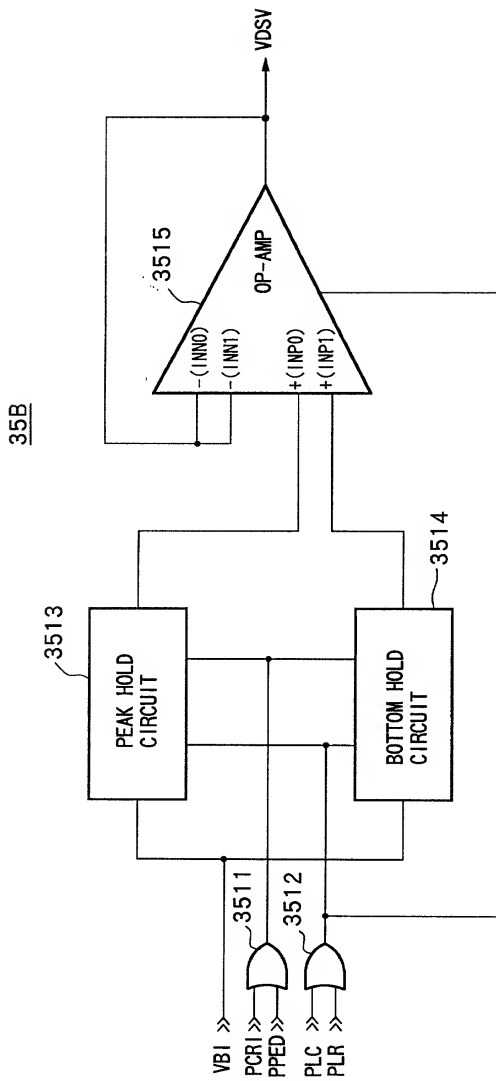
35A

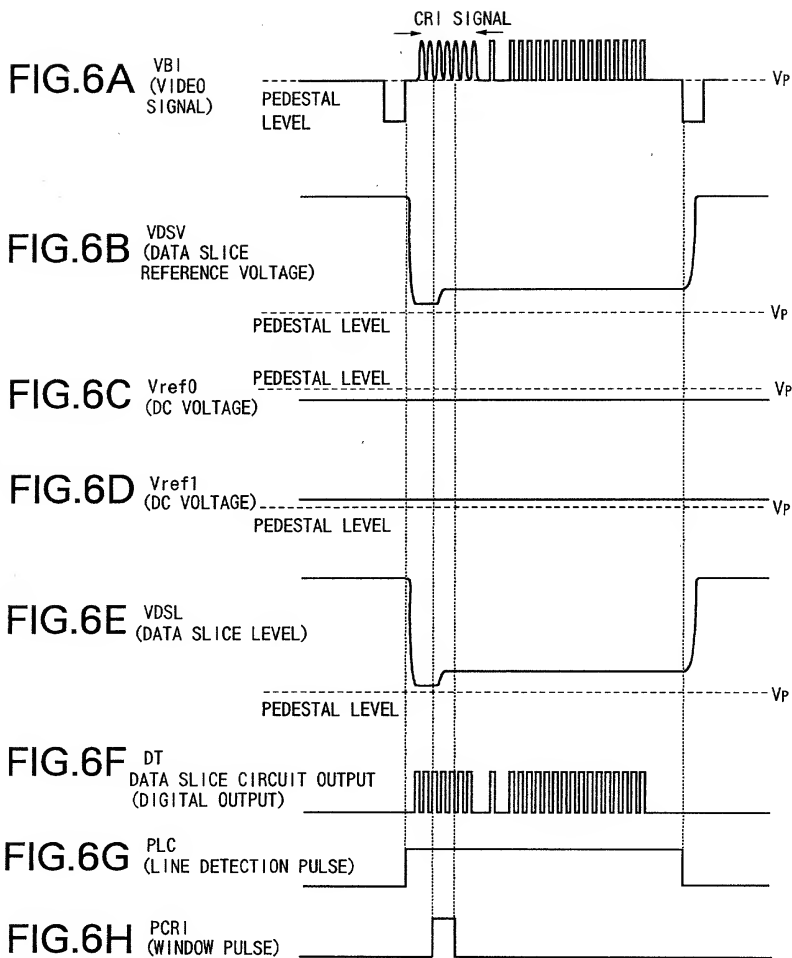


3502

PLC
PLR

FIG. 4





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FIG.7A VBI
(VIDEO
SIGNAL)

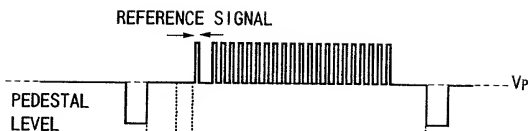


FIG.7B VDSV
(DATA SLICE
REFERENCE VOLTAGE)

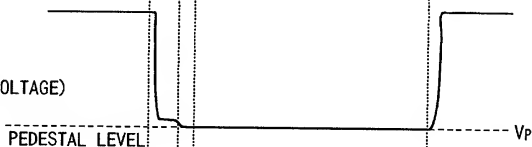


FIG.7C Vref0
(DC VOLTAGE)

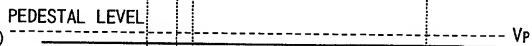


FIG.7D Vref1
(DC VOLTAGE)

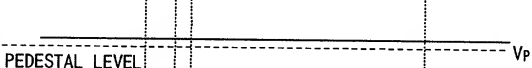


FIG.7E VDSL
(DATA SLICE LEVEL)

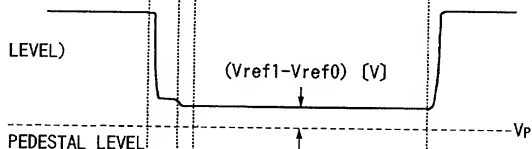


FIG.7F DT
(DATA SLICE CIRCUIT OUTPUT
(DIGITAL OUTPUT))



FIG.7G PLR
(LINE DETECTION PULSE)

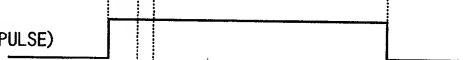


FIG.7H PPED
(WINDOW PULSE)



FIG.8

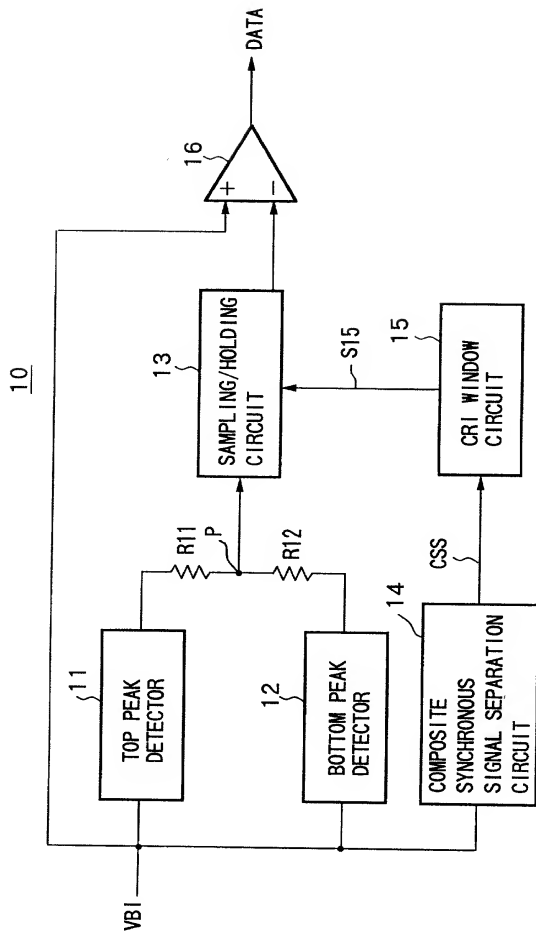


FIG. 9

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